

AMENDMENTS TO THE ABSTRACT:

Please amend the Abstract as follows:

ABSTRACT OF THE DISCLOSURE

A memory configuration for use in a computer system, ~~the memory comprising includes~~
a plurality of address decoders each of which is allocated an identifier having a predetermined
number of bits, each bit having first and second selectable states. A, ~~and a~~ data memory having
a plurality of word lines of predetermined length, is also included in each of the ~~said~~ address
decoders ~~being and is~~ activatable to select one of the plurality of word lines. The, ~~and the~~
address decoders ~~comprising means to~~ receive an input address having a predetermined number
of bits and ~~means to~~ compare the identifier of an address decoder with the input address wherein
the memory further ~~comprises means to~~ activates an address decoder if at least a predetermined
minimum number of bits set to the first selectable state in the input address correspond to bits set
to the first selectable state in the decoder identifier.